P24677 Form PTO-144	0			11.					C	Ann Dada No		1/88	Ta : 111 . 10		et 1 of 2	
Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office									k Office	Atty. Docket No. P24677			Serial No. 10/748,242			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT										Applicant Ryutaro YAMANAKA et al						
(Use several sh		iſ'n	ece.	ssary	)					Filing Date Group December 31, 2003 2133						
U.S. PATENT	DC	CU	ME	INTS	;					-						
EXAMINER INITIAL		D	00	UM	ENT	NL	JME	BER	DATE	CLASS			FILING DATE S IF APPROPRIATE			
		6	1	9	5	7	8	2	02/27/01	RAHMATULLAH et a						
	Γ	6	2	5	7	7	5	6	07/10/01	ZARUBINSKY et al						
		6	4	7	7	6	6	1	11/05/03	YAMANAKA						
		5	6	3	3	8	9	7	05/27/97	FETTWEIS et al						
		5	9	2	3	7	1	3	07/1999	HATAKEYAMA						
		5	3	2	7	4	4	0	07/1999	FREDRICKSON et al			***			
		5	9	1	2	9	0	8	06/1999	CESARI et al						
		5	9	8	7	4	9	0	11/1993	ALIDINA et al						
		5	2	2	0	5	7	0	06/1993	LOU et al	i					
	L	5	4	1	4	7	3	8	05/1995	BIENZ						
		5	7	1	0	7	8	4	01/1998	KINDRED et al						
		5	0	2	7	3	7	4	01/1991	ROSSMAN						
FOREIGN PA	TEI	YT I	00	CUN	IEN	TS										
		DOCUMENT NUMBER DA							DATE	COUNTRY CLASS			SUBCLASS	TRANSLATION NO		
0	9		ı	4	8	9	4	3	06/06/97	JAPAN				х		
6	3	-	2	ī	5	2	2	7	09/07/88	JAPAN				х		
	6	-	1	6	4	4	2	3	06/10/94	JAPAN						
		8	-	8	4	0	8	2	09/14/94	JAPAN						

OTHER	DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)
<u> </u>	English Language Abstract JP 09-148943.
	2 English Language Abstract JP 63-215227.
	3 UEDA et al., "A 16-bit Digital Signal Processor with Specially Arranged Multiply-Accumulator for Low Power Consumption",
	IEICE Trans. Fluction, Vol. E78-C, No. 12, December 1995

11/17/95 JAPAN

04/24/98 JAPAN

EXAMINER

\*EXAMINER: Initial if cliation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

DATE CONSIDERED

			_														
Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office												Atty. Docket No. Scrial No. P24677 10/748,242					
1	NFO			В	ΥA	PPI.	ICA	NT	STATEMEN	T		Applicant Ryutaro YAMANAKA ct al					
		(	Use	seve	eral	shee	ts if	nece	ssary)			Filing Date Group December 31, 2003 2133					
U.S. PATENT	U.S. PATENT DOCUMENTS																
EXAMINER	Γ						_			I					FILIN	G DATE	
INITIAL	L	DOCUMENT NUMBER DATE										3	CLASS	SUBCLASS		ROPRIATE	
	L	4	6	0	6	0	2	7	09/1986	OTANI							
		5	7	4	2	6	2	1	08/1998	AMON et al							
		6	2	6	3	4	7	4	07/17/01	WATANABE							
	-	5	8	8	1	1	0	6	03/09/99	CARTIER							
		6	3	3	0	6	8	4	12/2001	YAMANAKA	A et al						
		5	8	0	9	0	7	1	09/1998	KOBAYASH	l et al						
		5	5	0	9	0	2	ı	04/1996	TODOROKI							
	L	5	5	0	2	7	3	5	03/1996	COOPER							
		5	3	7	5	1	2	9	12/1994	COOPER							
		5	3	7	9	3	0	6	01/1995	NOMA et al							
	L	4	6	1	4	9	3	3	09/1986	YAMASHITA et al							
	L	6 1 2 5 1 5 3 09/26/00 SUGISAWA								SUGISAWA	et al						
FOREIGN PA	TE	NT I	00	CUM	IEN	TS											
		_													TRANSLA		
	-										OUNTI	ĮΥ	CLASS	SUBCLASS	YES	NO	
1	0	-	2	0	9	8	8	2	08/07/98	JAPAN						<b> </b>	
	1	1	Ŀ	5	5   1   3   0   02/26/99   JAPAN												
OTHER DOC	UM				_		******										
	L	4 H. LOU, "Viterbi Decoder Design for the IS-95 CDMA Forward Link", IEEE 46th Vehicular Technology Conference, No. 2, 1996, pp.1346-1350.															
	L	5 English Language Abstract of JP6-164423.															
	L	6 English Language Abstract of JP8-84082.															
	L	7 English Language Abstract of JP9-148943.															
	L	8 English Language Abstract of JP10-107651.															
		9 English Language Abstract of 10-209882.															
	1	0 English Language Abstract of 11-55130.															
EXAMINER											DAT	DATE CONSIDERED					

<sup>\*</sup>EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.